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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/577,547	05/24/2000	Cornelis G.M. Van Asma	PHn 17,450	5029

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PHILIPS INTELLECTUAL PROPERTY & STANDARDS  
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BRIARCLIFF MANOR, NY 10510

EXAMINER

TRAN, TRANG U

ART UNIT PAPER NUMBER

2614

DATE MAILED: 06/08/2004

19

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

09/577,547

Applicant(s)

VAN ASMA, CORNELIS G.M.

Examiner

Trang U. Tran

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 17 March 2004.  
2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.  
3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1 and 4-10 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.  
6) ☒ Claim(s) 1 and 4-10 is/are rejected.  
7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.  
8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.  
10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☒ All b) ☐ Some \* c) ☐ None of:  
1. ☒ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)  
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.  
4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_.  
5) ☐ Notice of Informal Patent Application (PTO-152)  
6) ☐ Other: \_\_\_\_\_.

## **DETAILED ACTION**

### ***Continued Examination Under 37 CFR 1.114***

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on April 15, 2004 has been entered.

### ***Response to Arguments***

2. Applicant's arguments with respect to claims 1 and 4-10 have been considered but are moot in view of the new ground(s) of rejection.

### ***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1 and 4-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over William Werner (EP 0 840 522) in view of Min (US Patent No. 6,486,918).

In considering claim 1, William Werner discloses all the claimed subject matter, note 1) the claimed processor for processing the input signal to the output data signal is met by the decoding engine 24 (Fig. 2, col. 5, lines 25-51 and col. 6, lines 27-54), 2) the claimed memory means for storing the input signal prior to supply to said processor is

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met by the share memory 23 (Fig. 2, col. 5, line 52 to col. 6, line 26), and 3) the claimed a memory manager coupled with the processor and the memory means, the memory manager being arranged to transfer the input data signals to the memory means and to transfer the stored input data signal from the memory means to the processor, respectively, wherein an output of the processor is coupled to an input of the memory manager, wherein the memory manager is further arranged to transfer the output data signal from the processor to the memory means for storing the output data signal, and wherein the memory manager is further arranged to transfer the stored data output signal from the memory means to the processor, and said processor is further arranged to execute a further operation on the stored data output signal, wherein said processor and said memory manager are arranged to execute different processes in time multiplex is met by the memory manager 22 which receives the compressed video data and stores it in memory 23, memory manager 22 handles the task of providing the appropriate data at the appropriate times to decoding engine 24 and scaling engine 25, the specific tasks of memory manager 22 are to: **extract compressed data from interface 11 and place it in memory 23, move compressed data from memory 23 to decoding engine 24, move decompressed data from decoding engine 24 back into memory 23, move decompressed data from memory 23 to scaling engine 25 and arbitrate any conflicting requests for data from the decoding engine 24 and the scaling engine 25** (Figs. 1 and 2, col. 5, line 52 to col. 6, line 11 and col. 7, lines 41-57).

However, William Werner explicitly does not disclose the claimed transfer the stored output data signal from the memory means to the display unit.

Min teaches that a decoder 20 sequentially decodes encoded video frame data in response to a control signal CS1 of a controller 10, a memory 30 sequentially stores the video frame data generated from the decoder 20 by a unit of the frame data in response to another control signal CS2 of the controller 10, and then supplies the stored frame data to another storage device 50 or a display unit 40, for example, a television receiver or a monitor (Fig. 3a, col. 1, line 57 to col. 2, line 5).

Therefore, it would have been obvious to one ordinary skill in the art at the time of the invention to incorporate of transferring the stored output data signal from the memory means to the display unit as taught by Min into William Werner's system in order to store video frame data in a memory which can reduce the capacity of a memory used in a decoding part of a video apparatus.

In considering claim 4, the claimed wherein said processor, said memory manager and the memory means are comprised on a single integrated circuit is met by Fig. 3, col. 7, lines 41-57 of William Werner.

In considering claim 5, the claimed wherein said processor is arranged to resize an image represented by said input data signal is met by the scaling engine 25 (Fig. 3, col. 6, line 55 to col. 7, line 40 col. 7, lines 41-57 of William Werner).

In considering claim 6, the claimed wherein said processor is further arranged to convert said input data signal representing an image into a color-sequential output signal is met by the SLM 16 which generates an image for each of three different colors of a picture and these three images are sequentially displayed through a color wheel 17 (Fig. 1, col. 4, line 38 to col. 5, line 20 of William Werner).

In considering claim 7, the claimed wherein said processor is arranged to convert said input data signal representing an image into a sub-field modulated output signal for controlling the display unit is met by the pulse-width modulation techniques (col. 1, line 13 to col. 2, line 27 of William Werner).

In considering claim 8, the claimed wherein a part of the memory means is arranged as a cyclic memory for storing a part of an image represented by the input data signal is met by the memory 23 (Fig. 2, col. 6, lines 12-26 of William Werner).

In considering claim 9, the claimed wherein the memory manager has a further input for a second data input signal, and the memory manager is arranged to transfer the second data input signal to the memory means is met by the memory manager 22 which receives the control signals from the control interface 21 (Fig. 2, col. 5, lines 46-51, col. 6, lines 21-26 of William Werner).

In considering claim 10, the claimed a digital video-processing unit having an output for providing the output data signal and a display system coupled to the output of said digital video-processing unit for displaying of said output data signal is met by the display optics 18 and the screen (Fig. 1 of William Werner).

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Trang U. Tran** whose telephone number is **(703) 305-0090**.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **John W. Miller**, can be reached at **(703) 305-4795**.

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**Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

**(703) 872-9314 (for Technology Center 2600 only)**

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

TT  
May 31, 2004

  
TRANG TRAN  
PATENT EXAMINER